

JC996 U.S. PRO
09/847548

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		Subclass
	Class	ISSUE CLASSIFICATION

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Jc3
866

U.S. UTILITY Patent Application

O.I.P.E. **PATENT DATE**
AD **fec3** **Q.A.** **1/1/00**

APPLICATION NO. 09/847548	CONT/PRIOR F	CLASS 356 436	SUBCLASS 311 86	ART UNIT 2877	EXAMINER - J A Jelma - JAT
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CANTS
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Hidekatsu Yoneda

Sample chip analyzing device and method for analyzing the sample

PTO-2040
12/89

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <hr/> <hr/>	<hr/> <div style="text-align: center;">(Assistant Examiner) _____ (Date) _____</div> <hr/> <div style="text-align: center;">(Primary Examiner) _____ (Date) _____</div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <hr/> <hr/>				ISSUE FEE	
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